

**U.S. UTILITY Patent Application**

<p align="center"><b>O.I.P.E.</b></p> <p>AK <i>HW</i> <i>aa. BW</i></p> <p>SCANNED</p>	<p align="center"><b>PATENT DATE</b></p> <p align="center">JUN 11 2001</p>
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APPLICATION NO. 09/705928	CONT/PRIOR	CLASS 257	SUBCLASS 83	ART UNIT 2811 314	EXAMINER <i>n. n. g. d.</i> <i>Q. Chandra</i>
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APPLICANTS

TITLE

Woon-Yong Park

Thin film transistor array panel for liquid crystal display

PTO-2040  
12/99

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257	59	257	71	72	296	300	350
INTERNATIONAL CLASSIFICATION							
H01L	29/04						
H01L	31/20						
H01L	31/036						
H01L	31/0376						
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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg. <u>18</u> Figs. Drwg. <u>18</u> Print Fig. <u>1</u>	<b>CLAIMS ALLOWED</b> Total Claims <u>11</u> Print Claim for O.G. <u>1</u>	
			<b>NOTICE OF ALLOWANCE MAILED</b> <u>11-30-01</u>
			<b>ISSUE FEE</b> (w) Amount Due <u>1280.00</u> Date Paid <u>2-20-02</u>
			<b>ISSUE BATCH NUMBER</b>
<p><i>11-13-01</i> (Date)</p> <p><i>Sharon Burke</i> (Legal Instruments Examiner)      <i>11/30/01</i> (Date)</p>			

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